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Term	Documents
(10 AND 1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	8
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<u>L22</u>	11 and 110	8	<u>L22</u>
<u>L21</u>	11 and 19	0	<u>L21</u>
<u>L20</u>	L18 and 110	7	<u>L20</u>
<u>L19</u>	L18 and 19	0	<u>L19</u>
<u>L18</u>	L1 and (laten\$3 or delay\$3 or wait\$3)	305	<u>L18</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L17</u>	18 and 111	5	<u>L17</u>
<u>L16</u>	18 and 110	5	<u>L16</u>
<u>L15</u>	18 and 19	0	<u>L15</u>
<u>L14</u>	18 and 16	4	<u>L14</u>
<u>L13</u>	18 and 15	11	<u>L13</u>

<u>L12</u> 18 and 14	21	<u>L12</u>
<u>L11</u> (712/31)[CCLS]	83	<u>L11</u>
<u>L10</u> (712/31)![CCLS]	83	<u>L10</u>
<u>L9</u> (709/210)[CCLS]	45	<u>L9</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L8</u> L7 and (laten\$3 or delay\$3 or wait\$3)	193	<u>L8</u>
<u>L7</u> L1 and (halt\$3 or suspend\$4 or inhibit\$3 or lock\$3)	224	<u>L7</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<u>L6</u> (709/208-211)[CCLS]	846	<u>L6</u>
<u>L5</u> (712/23-37)![CCLS]	2636	<u>L5</u>
<u>L4</u> (712/2-300)[CCLS]	11414	<u>L4</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L3</u> L2 and (laten\$3 or delay\$3 or wait\$3)	135	<u>L3</u>
<u>L2</u> L1 and (halt\$3 or suspend\$4 or inhibit\$3)	163	<u>L2</u>
<u>L1</u> Master\$1 near18 slave\$1 near35 (common\$2 or shar\$6) near4 (memor\$4 or stor\$5)	580	<u>L1</u>

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IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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**1. Multiprocessor shared-memory information exchange**

Santoline, L.L.; Bowers, M.D.; Crew, A.W.;  
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Robles, O.D.; Bosque, J.L.; Pastor, L.; Rodriguez, A.;  
Computer Architecture for Machine Perception, 2005. CAMP 2005. Proceedings. Seventh Internati  
04-06 July 2005 Page(s):309 - 314  
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Rizk, N.J.;  
Parallel and Distributed Processing Symposium, 2003. Proceedings. International  
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- ☐ 7. **A machine-independent approach to parallel programming**  
Riccardi, G.; Traversat, B.; Chandra, U.;  
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- ☐ 9. **A VHDL approach to system design for multiprocessor circuit simulation**  
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Issue 0, Part 20000, 19-21 Oct. 1993 Page(s):1182 - 1185 vol.2  
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- ☐ 10. **Modelling the vocal tract using multiple digital signal processors**  
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